



Docket No.: 060188-0802

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Akio HIRATA	:	Confirmation Number: 1762
Application No.: 10/800,711	:	Group Art Unit: 2824
Filed: March 16, 2004	:	Examiner: Dinh, Son T
For: SEMICONDUCTOR MEMORY AND SEMICONDUCTOR INTEGRATED CIRCUIT	:	

**AMENDMENT**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The following amendments and remarks are submitted in response to the Notice of Allowance mailed April 19, 2005. A Request for Continued Examination is being filed concurrently with the filing of this Amendment. Please amend the application as follows: